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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/790,812	03/03/2004	Tsung-Hsi Yu	3167-181	1487

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EXAMINER

NGUYEN, THANH NHAN P

ART UNIT	PAPER NUMBER
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2871

DATE MAILED: 07/26/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No. 10/790,812	Applicant(s) YU	
	Examiner (Nancy) Thanh-Nhan P. Nguyen	Art Unit 2871	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) ☒ Responsive to communication(s) filed on 15 May 2006.
- 2a) ☒ This action is **FINAL**.      2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) ☒ Claim(s) 1, 4-16 and 23-28 is/are pending in the application.  
 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 1 and 4-16 is/are allowed.
- 6) ☒ Claim(s) 23-27 is/are rejected.
- 7) ☒ Claim(s) 28 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 03 March 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
 a) ☒ All    b) ☐ Some \*    c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- |  |   |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892)   | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

### DETAILED ACTION

This communication is responsive to Amendment dated 5/15/2006.

Claims 17-22 are canceled. Claims 23-28 are newly added. Accordingly, claims 1, 4-16 and 23-28 are pending for the examination.

#### **Claim Rejections - 35 USC § 103**

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

**Claims 23, 26 and 27 are rejected under 35 U.S.C. 103(a) as being unpatentable over admitted prior art (admission) in view of Yarita et al (US Patent No. 6,411,353).**

Regarding claims 23 and 27, Admission discloses a liquid crystal display comprising: a liquid crystal display panel (300); a plastic frame (200), supporting said liquid crystal display panel; a metal cover (100), boxing said plastic frame therein and forming an interior space to accommodate said liquid crystal display panel; a print circuit board (400), fixed on a lower surface of said plastic frame and connecting to said liquid crystal display panel by a flexible flat cable (410) extending along a side wall of said plastic frame, having a grounding pin (430) extends from an edge of said print board to the outside, [see figs. 1 & 2].

Admission lacks disclosure of a conductive film, formed on a the grounding pin of said print circuit board, wherein the grounding pin is formed on a lower surface of

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printed circuit board, and another sidewall of said metal cover for transmitting segregated charges on said print circuit through said metal cover to environment; wherein a surface area of the conductive film being substantially smaller than that of the printed circuit board.

Yarita et al discloses a conductive film (MTP), formed on a grounding pin (GNDPT) of the print circuit board (FPC2), wherein the grounding pin is formed on a lower surface of said print circuit board, and another sidewall of said metal cover (SHD), [fig. 1], for the benefit of being easy to electrically connect the grounding pattern of a flexible circuit board to the upper frame even if the width of the flexible circuit board becomes narrower along with the reduction of the frame border area, [col. 5, lines 10-15]. Therefore, at the time the invention was made, it would have been obvious to a person of ordinary skill in the art to have a conductive film formed on a grounding pin of said print circuit board and another sidewall of said metal cover for the benefit of being easy to electrically connect the grounding pattern of a flexible circuit board to the upper frame even if the width of the flexible circuit board becomes narrower along with the reduction of the frame border area. Further, in fig. 9B, the printed circuit board (FPC2) has the length about the same length of liquid crystal display panel, therefore, even though Yarita et al does not explicitly disclose a surface area of the conductive film being substantially smaller than that of the printed circuit board, it would have been obvious to one ordinary skill in the art to have a surface area of the conductive film substantially smaller than that of the printed circuit board so that the conductive film would not take much space in the whole device.

**Regarding claim 26**, Admission discloses wherein said two grounding pins (430) are formed at opposite edges of said print circuit board (400) without connecting flexible flat cables (410), [see fig. 2].

**Claims 24 and 25 are rejected under 35 U.S.C. 103(a) as being unpatentable over admitted prior art (admission) in view of Yarita et al as discussed above and further in view of Levanon (US 6,855,441).**

**Regarding claims 24 and 25**, Admission lacks disclosure of the conductive film is taped on the grounding pin and metal cover by gluing; the conductive film is a conductive tape with both surfaces gluey, in which one surface of said conductive tape is taped on the grounding pin of said print circuit board and the sidewall of said metal cover and the other surface is used to glue a passivation film on a lower surface of said print circuit board to form an electric shielding upon devices on said print circuit board.

It was conventional at the time to use conductive tape with the surface(s) gluey to glue element(s) together as an intended use, as evidenced by Levanon, [fig. 3b, element '42'], and therefore does not patentably distinguish the invention.

***Allowable Subject Matter***

Claims 1 & 4-16 are allowed.

Claim 28 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter: None of prior art taught or disclosed:

Claims 1, 4-10: a liquid crystal display comprising: a print circuit board assembled in a liquid crystal display, fixed on a lower surface of plastic frame and connecting to said liquid crystal display panel by a flexible flat cable extending along a sidewall of said plastic frame; a passivation film, taped on a lower surface of print circuit board as an electric shielding and extending further to cover the flexible flat cable.

Claims 11-16 & 28: a print circuit board assembled in a liquid crystal display comprising: a plurality of flexible flat cables, extending from an edge of the print circuit board to a liquid crystal display panel; a grounding pin formed on the print circuit board; a passivation film, covering an exposed surface of the print circuit board as an electric shielding; and a conductive film, taped on both said grounding pin and a metal cover of the liquid crystal display to transport segregated charges on the print circuit board to environment, and taped around the print circuit board (to fix said passivation film).

### ***Response to Arguments***

Applicant's arguments with respect to claims 23-28 have been considered but are moot in view of the new ground(s) of rejection.

### ***Contact Information***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to (Nancy) Thanh-Nhan P. Nguyen whose telephone number is 571-272-1673. The examiner can normally be reached on Monday to Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on 571-272-1787. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

(Nancy) Thanh-Nhan P Nguyen

Examiner

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ANDREW SCHECHTER  
PRIMARY EXAMINER